

4.0 STANDARD TEST PROCEDURE

4.2 HARDWARE SETUP

BEFORE LOADING THE PROGRAM, THE FOLLOWING STEPS MUST BE DONE:

- A: POWER THE COMPUTER DOWN
- B: UNPLUG THE MB317 MODULE FROM THE COMPUTER
- C: PLUG THE QUAD EXTENDER INTO THE SLOT THE M8317 OCCUPIED
- D: PLUG THE MS311 MODULE INTO THE QUAD EXTENDER
- E: SET ALL THE SWITCHES ON THE M8317 MODULE TO THE OFF POSITION
- F: IF THE OPTION L + 2 TEST MODULE IS TO BE USED DO THE FOLLOWING, IF NOT GO TO STEP G IN THIS SECTION,
  - 1: TAKE ONE END OF THE IC SOCKET CONNECTOR CABLE AND PLUG IT INTO E93 ON THE MB317 MODULE(OBSERVING PIN 1 ORIENTATION),
  - 2: TAKE THE OTHER END OF THE CABLE AND PLUG IT INTO TS-2 (FIRST SOCKET ABOVE E70) ON THE G5041 MODULE
  - 3: TAKE ONE END OF THE NEXT IC SOCKET CONNECTOR CABLE AND PLUG IT INTO E88 ON THE M8317 MODULE
  - 4: TAKE THE OTHER END OF THE CABLE AND PLUG IT INTO TS-1 (SECOND SOCKET ABOVE E63) ON THE G5041 MODULE
  - 5: PLUG THE OPTION 1 + 2 TEST MODULE(G5041) INTO THE COMPUTER,
- G: POWER THE COMPUTER BACK UP
- H: GO TO PARAGRAPH 4.3. LOADING THE PROGRAM,

4.3 LOADING THE PROGRAM

COMPUTERS WITH 4K OF MEMORY WILL USE THE BINARY PAPER TAPE LABELED MAINDEC-08-DJKMA-B-PB1, COMPUTERS WITH LESS THAN 4K OF MEMORY WILL USE THE FOUR 1K SEGMENTED RIM PAPER TAPES WHICH ARE LABELED AS FOLLOWS:

- 1 MAINDEC-08-DJKMA-B-PM1 1K PART 1
- 2 MAINDEC-08-DJKMA-B-PM2 1K PART 2
- 3 MAINDEC-08-DJKMA-B-PM3 1K PART 3
- 4 MAINDEC-08-DJKMA-B-PM4 1K PART 4

A: IF THE COMPUTER CONTAINS 4K OF MEMORY OR MORE: DO STEP B, OTHERWISE, DO STEP C BELOW FOR COMPUTERS WITH LESS THAN 4K OF MEMORY

B: LOAD THE BINARY TAPE MENTIONED ABOVE USING THE STANDARD BINARY LOADER TECHNIQUE, AFTER THE TAPE HAS BEEN SUCCESSFULLY LOADED GO TO PARAGRAPH 4.4, PROGRAM INITIALIZATION

C: TO LOAD THE 1K SEGMENTED RIM PAPER TAPES MENTIONED ABOVE, DEPOSIT INTO LOCATIONS LISTED BELOW THE APPROPRIATE RIM LOADER FOR THE LOADING DEVICE TO BE USED.

|                   |                  |
|-------------------|------------------|
| HIGH SPEED READER | LOW SPEED READER |
| -----             | -----            |
| .....             | .....            |

D: PLACE THE APPROPRIATE 1K SEGMENT INTO THE READER, LOAD ADDRESS TO 0156, PRESS INIT AND THEN RUN

E: WHEN THE TAPE HAS BEEN LOADED, STOP THE COMPUTER, GO TO PARAGRAPH 4.4, PROGRAM INITIALIZATION

4.4 PROGRAM INITIALIZATION

THE PROGRAM WHEN LOADED IS INITIALIZED TO RUN WITHOUT THE HARDWARE FRONT PANEL SWITCH REGISTER, WITHOUT OPTION 1 + 2 TEST MODULE, AND THE AMOUNT OF MEMORY REQUIRED TO RUN THE PROGRAM (4K FOR THE COMPLETE PROGRAM AND 1K FOR THE SEGMENTED 1K VERSIONS OF THE PROGRAM), IF IT IS DESIRED TO CHANGE THE HARDWARE CONFIGURATION, LOAD ADDRESS TO 0021 AND DEPOSIT INTO THIS LOCATION THE APPROPRIATE HARDWARE CONFIGURATION FOR THE BITS LISTED BELOW:

NOTE: IF MEMORY SIZE IS LARGER OR SMALLER THAN LISTED ABOVE, IT SHOULD BE CHANGED IN LOCATION 0021,

BIT 0 = 0 THE PROGRAM WILL USE LOCATION 0020 AS A PSEUDO SWITCH REGISTER  
BIT 0 = 1 THE PROGRAM WILL USE THE HARDWARE FRONT PANEL SWITCH REGISTER

BIT 2 = 1 HAS A M8317 OPTION 2 MODULE

BIT 4 = 0 THE PROGRAM WILL NOT USE THE OPTION 1 + 2 TEST MODULE TO TEST THE M8317  
BIT 4 = 1 THE PROGRAM WILL USE THE OPTION 1 + 2 TEST MODULE TO TEST THE M8317

BIT 5 = 0 NOT RUNNING ON THE POP-8A XOR TESTER  
BIT 5 = 1 RUNNING ON PDP-8A XOR TESTER - BIT 4 MUST BE SET TO A 1 AND THE OPTION 1 + 2 TEST MODULES MUST BE USED

BITS 7-11 SPECIFIES THE PDP-8A'S MEMORY SIZE, ALL ZEROES INDICATES 1K OF MEMORY, AN ADDITION OF 1 TO THE NUMBER IN BITS 7-11 INCREASES MEMORY SIZE BY 1K,

GO TO PARAGRAPH 4.5, MEMORY EXTENSION/TIME SHARE TEST

4.5 RUN MEMORY EXTENSION/TIME SHARE TEST

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THE TAPE(S) TO BE USED TO RUN THIS TEST ARE AS FOLLOWS:

COMPUTERS WITH AT LEAST 4K OF MEMORY

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MAINDEC -08-DJKMA-B-PB1

COMPUTERS WITH LESS THAN 4K OF MEMORY

-----  
MAINDEC -08-DJKMA-B-PM1  
MAINDEC -08-DJKMA-B-PM2

NOTE: IF OPTION 1 + 2 TEST MODULE IS SELECTED AND THE COMPUTER CONTAINS 4K OR MORE MEMORY, THIS TEST IS THE ONLY TEST REQUIRED TO BE RUN WITH THE 4K PROGRAM LISTED ABOVE,

A: LOAD ADDRESS TO ONE OF THE FOLLOWING ADDRESSES FOR THE TAPE(S) TO BE RUN

ADDRESS 0200 (RESTART 0201 IF OPTION 1 + 2 TEST MODULE IS USED) MAINDEC-08-DJKMA-B-PB1  
ADDRESS 0200 MAINDEC-08-DJKMA-B-PM1  
ADDRESS 0200 MAINDEC-08-DJKMA-B-PM2

B: SET THE SWITCH REGISTER OR PSEUDO SWITCH REGISTER, WHICHEVER WAS SELECTED, TO 0000

C: PRESS "INIT" AND THEN "RUN"

D: SETTING THE SWITCH REGISTER OR PSEUDO SWITCH REGISTER, WHICHEVER WAS SELECTED, TO 0400 WILL CAUSE THE COMPUTER TO HALT AT THE END OF A PROGRAM PASS, THE LOCATION AT WHICH IT WILL HALT, WILL BE ONE OF THE FOLLOWING FOR THE TAPE THAT IS BEING RUN:

LOCATION 1463 • MAINDEC-08-DJKMA-B-PB1  
LOCATION 1634 • MAINDEC-08-DJKMA-B-PM1  
LOCATION 1634 • MAINDEC-08-DJKMA-B-PM2

E: THE PROGRAM WILL NOW RUN UNTIL AN ERROR IS ENCOUNTERED OR THE PROGRAM IS STOPPED BY THE OPERATOR OR SR3=1,

F: AN ERROR MAY RESULT IN AN ERROR HALT OR A JMP SELF

4.6 RUN TIME SHARE DISABLE TEST

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THE TAPE(S) TO BE USED TO RUN THIS TEST ARE AS FOLLOWS:

COMPUTERS WITH AT LEAST 4K OF MEMORY

-----  
MAINDEC-08-DJKMA-B-PB1

COMPUTERS WITH LESS THAN 4K OF MEMORY

-----  
MAINDEC-08-DJKMA-B-PM3

A: ON THE M8317 MODULE, SET SWITCH 1 TO THE ON POSITION ON THE SWITCH PACKAGE WHICH LIES ABOVE I.C. E87, SETTING OF THIS SWITCH WILL DISABLE THE TIME SHARE LOGIC

B. LOAD ADDRESS TO ONE OF THE FOLLOWING ADDRESSES FOR THE TAPE TO BE RUN:

ADDRESS 4260 • MAINDEC-08-DJKMA-B-PB1  
ADDRESS 1260 • MAINDEC-08-DJKMA-B-PM3

C: SET SWITCH REGISTER OR PSEUDO SWITCH REGISTER, WHICHEVER WAS SELECTED, TO 0000, PRESS "INIT" AND THEN "RUN",

D: THE PROGRAM SHOULD HALT ON A SUCCESSFUL PASS AT LOCATION 4300 FOR MAINDEC-08-DJKMA-B-PB1 AND AT LOCATION 1300 FOR MAINDEC-08-DJKMA-B-PM3

E: SET THE SWITCH THAT WAS SET IN STEP A ABOVE TO THE OFF POSITION,

F: GO TO PARAGRAPH 4.7, RUN BOOTSTRAP/SIMULATOR TEST,

4.7 RUN BOOTSTRAP/SIMULATOR TEST

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IF A OPTION 1 + 2 TEST MODULE IS NOT USED WITH THE PROGRAM, GO TO PARAGRAPH 4.7.2 RUN BOOTSTRAP TEST,

IF A OPTION 1 + 2 TEST MODULE IS USED WITH THE PROGRAM AND THE COMPUTER CONTAINS LESS THAN 4K OF MEMORY, GO TO PARAGRAPH 4.7.1, RUN SIMULATOR TEST

4.7.1 RUN SIMULATOR TEST

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THE TAPE TO BE USED WITH THIS TEST IS MAINDEC-08-DJKMA-B-PM3,

THIS TEST USES THE POTION 1 + 2 TEST MODULE TO CHECK THE EMA LINES, TIME SHARE DISABLE, AC LOW AND BATTERY EMPTY FLIP-FLOPS.

- A: LOAD ADDRESS TO 0201
- B: SET THE SWITCH REGISTER OR THE PSEUDO SWITCH REGISTER, WHICHEVER WAS SELECTED, TO 0000
- C: PRESS "IN IT", AND THEN "RUN"
- D: THE PROGRAM WILL NOW RUN UN TILL AN ERROR IS ENCOUNTERED, STOPPED BY THE OPERATOR, OR SWITCH REGISTER 3 SET TO A 1.
- E: SETTING SWITCH REGISTER 3 TO A 1 WILL CAUSE THE COMPUTER TO HALT AT LOCATION 1640.
- F: WHILE RUNNING THIS PROGRAM THE RUN LIGHT WILL BE BLINKING ON AND OFF

4.7.2 RUN BOOTSTRAP TEST  
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THE TAPE(S) TO BE USED TO RUN THIS TEST ARE AS FOLLOWS:

COMPUTERS WITH AT LEAST 4K OF MEMORY

-----  
MAINDEC-08-DJKMA-B-PB1

COMPUTERS WITH LESS THAN 4K OF MEMORY

-----  
MAINDEC-08-DJKMA-B-PM3

NOTE: DISABLE OR UNPLUG FROM THE COMPUTER ANY DEVICES ASSOCIATED WITH THE BOOTSTRAPS.

- A: SET ALL THE SWITCHES ON THE M8317 MODULE TO THE OFF POSITION.
- B: SET THE SWITCHES S1-6,S1-7,S1-8 ON THE SWITCH PACKAGE WHICH LIES ABOVE I.C. E79 ON THE M8317 MODULE TO THE ON POSITION
- C: SET THE SWITCHES ON THE M8317 MODULE TO THE BOOTSTRAP TO BE TESTED FROM THE TABLE BELOW:

NOTE: ONLY THE RK8E AND RX8E BOOTSTRAPS CAN BE TESTED ON 1K COMPUTERS

WHEN REFERENCING SWITCHES IN THE TABLE BELOW, S2 IS THE SWITCH PACKAGE LOCATED ABOVE I.C. E87, AND S1 IS LOCATED ABOVE I.C. E79,

| BOOTSTRAP    | S2 SWITCHES |      |      |      | S1 SWITCHES |      |      |
|--------------|-------------|------|------|------|-------------|------|------|
| -----        | S2-5        | S2-6 | S2-7 | S2-8 | S1-1        | S1-2 | S1-3 |
| HI-LO PT RDR | ON          | ON   | ON   | OFF  | ON          | ON   | ON   |
| RK8E         | ON          | OFF  | ON   | OFF  | ON          | OFF  | ON   |
| RX8E         | ON          | OFF  | OFF  | ON   | OFF         | ON   | ON   |
| RF08/DF32D   | OFF         | ON   | OFF  | ON   | OFF         | ON   | OFF  |
| TA8E         | OFF         | ON   | OFF  | OFF  | OFF         | ON   | OFF  |

- D: LOAD ADDRESS TO ONE OF THE FOLLOWING ADDRESSES FOR THE TAPE THAT IS TO BE RUN:

ADDRESS 4465 = MAINDEC-08-DJKMA-B-PB1  
ADDRESS 1465 = MAINDEC-08-DJKMA-B-PM3

- E: PRESS "INIT" AND THEN "RUN", THIS WILL CLEAR THE BOOTSTRAP LOCATIONS IN MEMORY THAT THE BOOTSTRAPS WILL LOAD INTO.
- F: THE PROGRAM WILL HALT AT LOCATION 4515 FOR MAINDEC-08-DJKMA-B-PB1 OR 1515 FOR MAINDEC-08-DJKMA-B-PM3,
- G: TOGGLE THE BOOT SWITCH OR BOOT KEY, THE MODULE SHOULD DO A BOOTSTRAP AND THE COMPUTER SHOULD BE RUNNING.
- H: HALT THE COMPUTER AND LOAD ADDRESS TO ONE OR THE FOLLOWING ADDRESSES FOR THE TAPE THAT IS BEING RUN:  
  
ADDRESS 4400 = MAINDEC-08-DJKMA-B-PB1  
ADDRESS 1400 = MAINDEC-08-DJKMA-B-PM3
- I: THE PROGRAM WILL HALT AT ADDRESS 4400 FOR MAINDEC-08-DJKMA-B-PB1 OR 1400 FOR MAINDEC-08-DJKMA-B-PM3.
- J: SET TME SWITCH REGISTER OR THE PSEUDO SWITCH REGISTER, WHICHEVER WAS SELECTED, TO THE BOOTSTRAP TO BE COMPARED FROM THE TABLE BELOW:

| BOOTSTRAP    | S.R. SETTINGS |
|--------------|---------------|
| -----        | -----         |
| HI-LO PT RDR | 0000          |
| RF08/DF32D   | 0001          |
| TA8E         | 0002          |
| RX8E         | 0003          |

- K: PRESS "INIT" AND THEN "RUN",
- L: THE PROGRAM SHOULD HALT AT LOCATION 4461 FOR MAINDEC-08-DJKMA-B-PB1 OR 1461 FOR MAINDEC-08-DJKMA-B-PM3 IF THE BOOTSTRAP COMPARED OK.
- M: DO STEPS A THROUGH L FOR EACH BOOTSTRAP
- N: GO TO PARAGRAPH 4.8, RUN AUTO RESTART/POWER FAIL TEST,

4.8 RUN AUTO RESTART/POWER FAIL TEST

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 THE TAPE(S) TO BE USED TO RUN THIS TEST ARE AS FOLLOWS

COMPUTERS WITH AT LEAST 4K OF MEMORY

-----  
 MAINDEC-08-DJKMA-B-PB1

COMPUTERS WITH LESS THAN 4K OF MEMORY

-----  
 MAINDEC-08-DJKMA-B-PM4

THE BATTERY SUPPLY SHOULD BE FULLY CHARGED TO RUN THIS TEST

- A: SET ALL SWITCHES TO THE OFF POSITION ON THE M8317 MODULE,
- B: SET SWITCHES 1 3 6 7 AND 8 TO THE ON POSITION ON THE SWITCH PACKAGE WHICH IS LOCATED ABOVE E19 ON THE M8317 MODULE
- C: SET SWITCHES 5 AND 7 TO THE ON POSITION ON THE SWITCH PACKAGE WHICH IS LOCATED ABOVE E87 ON THE M8317 MODULE
- D: SET THE SWITCHES ON THE M8317 MODULE TO THE AUTO RESTART TO BE TESTED FROM THE TABLE BELOW

NOTE: ON 1 K COMPUTERS THE ONLY RESTARTS THAT CAN BE TESTED ARE AT 0000 AND 0200

| AUTO RESTART | S2 SWITCHES(ABOVE E87) |      |      |
|--------------|------------------------|------|------|
| -----        | S2-2                   | S2-3 | S2-4 |
| 0000         | OFF                    | OFF  | OFF  |
| 0200         | OFF                    | ON   | OFF  |
| 2000         | ON                     | OFF  | OFF  |
| 4200         | ON                     | ON   | OFF  |

- F: LOAD ADDRESS TO 4600 FOR MAINDEC-08-DJKMA-B-PB1 OR TO 0201 FOR MAINDEC-08-DJKMA-B-PM4
- G: PRESS "INIT" AND THEN "RUN"
- H: THE PROGRAM WILL NOW FILL A BUFFER AREA WITH A COMPLEMENTING 5252 DATA PATTERN, AND THEN HALT AT LOCATION 4640 FOR MAINDEC-08-DJKMA-B-PB1 OR AT 0227 FOR MAINDEC-08-DJKMA-B-PM4
- I: NOW SET THE SWITCH REGISTER OR THE PSEUDO SWITCH REGISTER, WHICHEVER WAS SELECTED, TO THE AUTO RESTART TO BE TESTED FROM THE TABLE BELOW

| AUTO RESTART | S.R. SETTINGS |
|--------------|---------------|
| -----        | -----         |
| 0000         | 0003          |
| 0200         | 0002          |
| 2000         | 0001          |
| 4200         | 0000          |

- J: PRESS "INIT" AND THEN "RUN"
- K: THE PROGRAM NOW STARTS COMPARING THE DATA THAT WAS PUT IN THE BUFFER AREA
- L: THE OPERATOR AT THIS TIME MUST UNPLUG THE AC LINE CORD. WHEN THE LINE CORD HAS BEEN UNPLUGGED, THE PROGRAM SHOULD HALT AT LOCATION 4763 FOR MAINDEC-08-DJKMA-B-PB1 OR AT LOCATION 0352 FOR MAINDEC-08-DJKMA-B-PM4
- M: WITH A MINIMAL AMOUNT OF DELAY, THE OPERATOR MUST PLUG THE AC LINE CORD BACK IN, AT THIS TIME THE M8317 SHOULD DO A AUTO RESTART TO THE AUTO RESTART SELECTED, THE PROGRAM THEN CHECKS FOR THE CORRECT AUTO RESTART AND THEN GOES BACK TO COMPARING DATA
- N: STEPS L AND M SHOULD BE REPEATED SEVERAL TIMES FOR EACH OF THE AUTO RESTARTS,

4.9 PDP-8A XOR TESTING

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5.0 ERRORS  
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5.1 MEMORY EXTENSION/TIME SHARE TEST ERRORS  
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ALL ERRORS DETECTED UNDER THIS TEST WILL RESULT IN A HALT, AN ERROR HALT OR A JMP SELF FOR THE TAPES LISTED BELOW

MAINDEC-08-DJKMA-B-PB1  
MAINDEC-08-DJKMA-B-PM1  
MAINDEC-08-DJKMA-B-PM2

REFER TO THE APPROPRIATE LISTING FOR THE ERROR, THE TEST BEING EXERCISED AND FOR THE TEST SEQUENCE BEING EXECUTED.

5.1.1 MEMORY EXTENSION/TIME SHARE TEST ERROR RECOVERY  
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REFER TO THE APPROPRIATE SECTION BELOW FOR THE ACTION TO BE TAKEN:

ERROR HALT ERRORS  
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A ERROR HALT IS WHEN THE COMPUTER HALTS AT LOCATION 5132 FOR PAPER TAPE MAINDEC-08-DJKMA-B-PB1 OR AT LOCATION 1717 FOR PAPER TAPES MAINDEC-08-DJKMA-B-PM1 AND -PM2, THE CONTENTS OF THE ACCUMULATOR FOR THIS ERROR HALT WILL CONTAIN THE LOCATION AT WHICH THE ERROR WAS DETECTED BY THE PROGRAM, REFER TO THE APPROPRIATE PROGRAM LISTING FOR THE CAUSE OF THE ERROR. SET THE SWITCH REGISTER TO 7000 AND PRESS "INIT" AND THEN "RUN". THERE MAY BE 1 OR MORE ERROR HALTS, IF THE ERROR WAS A DATA ERROR, OR THE OPTION 1 + 2 TEST MODULE WAS BEING USED, THE PROGRAM IS NOW IN A SCOPE LOOP.

HALT/JMP SELF ERRORS  
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ANY ERROR ENCOUNTERED DURING A TEST SEQUENCE WHICH RESULTS IN A HALT OR A JMP SELF, REPLACE THE HALT OR JMP SELF WITH A JMP TEST(X) (X=TEST BEING EXECUTED I.E. JMP TEST1, JMP TEST2, ETC,)

5.2 TIME SHARE DISABLE TEST ERRORS  
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ANY ERRORS DETECTED BY THIS TEST WILL RESULT IN A HALT AT LOCATION 5132 FOR TAPE MAINDEC-08-DJKMA-B-PB1, OR AT LOCATION 1733 FOR TAPE MAINDEC-08-DJKMA-B-PM3, THE CONTENTS OF THE AC WILL CONTAIN THE ADDRESS WHERE THE ERROR WAS DETECTED BY THE PROGRAM.

5.2.1 TIME SHARE DISABLE TEST ERROR RECOVERY  
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SET THE SWITCH REGISTER OR PSEUDO SWITCH REGISTER WHICHEVER WAS SELECTED AT PROGRAM INITIALIZATION TO 7000 AND PRESS "INIT" AND "RUN". THE PROGRAM IS NOW IN A SCOPE LOOP.

5.3 BOOTSTRAP TEST ERRORS  
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BOOTSTRAP ERRORS WILL BE GENERALLY OF TWO TYPES, WHICH ARE:  
1) FAILED TO DO A BOOTSTRAP; 2) BOOTSTRAP FAILED TO COMPARE,  
ANY ERRORS DUE TO 2 ABOVE WILL RESULT IN A ERROR HALT AT LOCATION 5132 FOR MAINDEC-08-DJKMA-B-PB1 OR AT LOCATION 1733 FOR MAINDEC-08-DJKMA-B-PM3, THE CONTENTS OF THE AC WILL CONTAIN THE ADDRESS WHERE THE ERROR WAS DETECTED BY THE PROGRAM.

5.3.1 BOOTSTRAP TEST ERROR RECOVERY  
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FOR FAILURE TYPE 1 ABOVE, CHECK FOR CORRECT SWITCH SETTINGS ON THE M8317 MODULE AND TRY AGAIN, IF THIS STILL DOES NOT PRODUCE A BOOTSTRAP, USE A SCOPE AND THE LOGIC PRINTS TO TROUBLE SHOOT THE ERROR.

FOR FAILURE TYPE 2 ABOVE, PRESSING CONTINUE 3 MORE TIMES WILL RESULT IN 3 MORE HALTS, WHICH WILL GIVE THE ADDRESS WHICH DIDN'T COMPARE, THE EXPECTED CONTENT OF THAT ADDRESS AND THE ACTUAL CONTENT OF THAT ADDRESS. IF THE OPTION 1 + 2 TEST MODULE WAS UTILIZED WITH THE PROGRAM, SET THE SWITCH REGISTER OR PSEUDO SWITCH REGISTER WHICH EVER WAS SELECTED TO 7000 AND PRESS "INIT" AND THEN "RUN". THE PROGRAM MAY HALT ONE MORE TIME AND THEN REPEAT THE SEQUENCE. THE PROGRAM IS NOW IN A SCOPE LOOP DOING THE BOOTSTRAPS, IF THE TEST MODULE WAS NOT USED, REPEAT THE BOOTSTRAP SEQUENCE SEVERAL TIMES, USING THE SCOPE AND LOGIC PRINTS TO TROUBLE SHOOT WITH.

5.4 AUTO RESTART/POWER FAIL TEST ERRORS  
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ANY ERRORS ENCOUNTERED DURING THIS TEST MAY BE DUE TO THE BATTERY BEING DISCHARGED, IMPROPER MODULE SWITCH SETUP, FAILURE TO DO A AUTO RESTART, A AUTO RESTART TO THE WRONG ADDRESS, OR A DATA COMPARE ERROR.

5.4.1 AUTO RESTART/POWER FAIL TEST ERROR RECOVERY  
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AFTER ASSURING THE MODULE TO BE SETUP CORRECTLY AND RETRYING THE TEST, USE A SCOPE AND THE LOGIC PRINTS TO TROUBLE SHOOT THE PROBLEM.

6.0 SWITCH REGISTER SETTINGS  
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6.1 NORMAL OPERATING SWITCHES  
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SR3=1 (0400) HALT PROGRAM AT COMPLETION OF A PROGRAM PASS

6.2 ERROR RELATED SWITCHES  
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SR0=1 (4000) INHIBIT ERROR HALT  
SR1=1 (2000) LOOP ON ERROR  
SR2=1 (1000) LOOP ON TEST SUCH AS TEST1, TEST2, ETC.

7,0 REVISIONS  
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8.0 PROGRAM DESCRIPTION  
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